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## Freeform Search

US Pre-Grant Publication Full-Text Database US Patents Full-Text Database US OCR Full-Text Database Database: EPO Abstracts Database JPO Abstracts Database Derwent World Patents Index IBM Technical Disclosure Bulletins L14 and (((simultaneous\$2 or concurrent\$2) with read\$3 with writ\$3) with (cache or ram or Term: buffer)) Display: 10 Documents in Display Format: TI Starting with Number 1 Generate: O Hit List O Hit Count O Side by Side O Image

Search History

Interrupt

Clear

DATE: Monday, August 16, 2004 Printable Copy Create Case

Search

Set Name side by side	Query	Hit Count	Set Nam result
DB=0	USPT; PLUR=YES; OP=OR		
<u>L23</u>	L21 and (((pipelin\$3 or stream\$3) near3 (cache or buffer or ram)) same ((simultaneous\$2 or concurrent\$2) with read\$3 with writ\$3))	4	<u>L23</u>
<u>L22</u>	L21 and ((pipelin\$3 or stream\$3) near3 (cache or buffer or ram))	268	<u>L22</u>
<u>L21</u>	114 or 18	3069	<u>L21</u>
<u>L20</u>	(5131080  5276851  5287487  5392407  5426739)![pn]	5	<u>L20</u>
<u>L19</u>	L8 and (((simultaneous\$2 or concurrent\$2) with read\$3 with writ\$3) with (fifo))	3	<u>L19</u>
<u>L18</u>	L1 and (((simultaneous\$2 or concurrent\$2) with read\$3 with writ\$3) with (fifo))	2	<u>L18</u>
<u>L17</u>	L14 and (((simultaneous\$2 or concurrent\$2) with read\$3 with writ\$3) with (fifo))	7	<u>L17</u>
<u>L16</u>	L14 and (((simultaneous\$2 or concurrent\$2) with read\$3 with writ\$3) with (cache or ram or buffer))	72	<u>L16</u>
<u>L15</u>	L14 and (((simultaneous\$2 or concurrent\$2) with read\$3 with writ\$3) same (cache or ram or buffer))	97	<u>L15</u>
<u>L14</u>	711/167,168,169.ccls.	1529	<u>L14</u>
<u>L13</u>	18 and ((ram adj buffer) same (simultaneous\$2 or concurrent\$2))	1	<u>L13</u>
<u>L12</u>	18 and (ram adj buffer)	11	<u>L12</u>
<u>L11</u>	5559952.pn. BEST AVAILABLE COPY	1	<u>L11</u> ·
<u>L10</u>	L8 and ((disk or drive) same (cache or ram) same ((simultaneous\$2 or concurrent\$2) with read\$3 with writ\$3))	16	<u>L10</u>

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Freeform Search		Pag	Page 2 o	
<u>L9</u>	L8 and ((simultaneous\$2 or concurrent\$2) with read\$3 with writ\$3)	150	<u>L9</u>	
<u>L8</u>	11 or 711/131,129.ccls.	1646	<u>L8</u>	
<u>L7</u>	L1 and (ram with buffer\$3 with threshold)	1	<u>L7</u>	
<u>L6</u>	15 and ((simultaneous\$2 or concurrent\$2) with read\$3 with writ\$3)	7	<u>L6</u>	
<u>L5</u>	L4 and 13	35	<u>L5</u>	
<u>L4</u>	L1 and (ram with buffer\$3 with writ\$3)	46	<u>L4</u>	
<u>L3</u>	L1 and (ram with buffer\$3 with read\$3)	48	<u>L3</u>	
<u>L2</u>	L1 and (ram with buffer\$3)	114	<u>L2</u>	
L1	711/111.112.ccls.	1256	<u>L1</u>	

## END OF SEARCH HISTORY

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